

2  
19274-0102



\$35.00 2N A  
PATENT APPLICATION

00 003420

1

SECURITY AND SURVEILLANCE SYSTEM

TECHNICAL FIELD OF THE INVENTION

This invention relates in general to intruder detection devices and more particularly to a security and surveillance system.

5

2

BACKGROUND OF THE INVENTION

5           Conventional security systems require expensive and bulky equipment to monitor a building or other desired areas for denying access to potential intruders. These systems are not practical for use in a residential home. Therefore, it is desirable to have a security and surveillance system that integrates with available home equipment such as a television to provide low cost and effective surveillance monitoring of the house and grounds.

10           From the foregoing it may be appreciated that a need has arisen for a security and surveillance system that automatically performs a specified function upon detecting the presence of an intruder. A need has also arisen for a security and surveillance system that automatically 15           interrupts normal television viewing to display a zone of surveillance in the event of a monitored intrusion. Further, a need has arisen for a security and surveillance system that generates an infrared code to control a television monitor in response to a detected intrusion 20           event.

SUMMARY OF THE INVENTION

5           In accordance with the present invention, a security and surveillance system is provided which substantially eliminates or reduces disadvantages and problems associated with conventional intrusion detection devices.

The present invention includes a detector for determining a presence in a zone of surveillance. Upon detection of an undesirable presence in the zone of surveillance, the detector generates an activation signal. 10           In response to the activation signal, a processor generates an infrared code that performs a specific function corresponding to the activation signal. The present invention may also include a video camera for generating an image of the zone of surveillance and a television monitor coupled to the video camera. The processor controls the television monitor through the specific function corresponding to the generated infrared code in order to display an image of the zone of surveillance generated from the video camera. 15

20           The security and surveillance system of the present invention provides for various technical advantages. For example, one technical advantage is to provide a security and surveillance system that uses an infrared code to perform a specific function. Another technical advantage is to automatically interrupt normal television viewing in order to display a zone of surveillance upon the detection of an intrusion event. Other technical advantages are readily apparent to one skilled in the art from the following descriptions, figures, and claims. 25

BRIEF DESCRIPTION OF THE DRAWINGS

For a more complete understanding of the present invention and the advantages thereof, reference is now made to the following description taken in conjunction with the accompanying drawings, wherein like reference numerals represent like parts, in which:

FIGURE 1 illustrates a block diagram of a security and surveillance system;

FIGURE 2 illustrates a block diagram of a processor implemented in the preferred security and surveillance system; and

FIGURE 3 illustrates a block diagram of a security and surveillance system wherein the processor performs another specified function.

DETAILED DESCRIPTION OF THE INVENTION

5 FIGURE 1 is a block diagram of a security and surveillance system 10. Security and surveillance system 10 includes a detector 12 monitoring a zone of surveillance A. Other detectors 14, 16, and 18 may be used to monitor zones of surveillance B, C, and D, respectively. Any type of detector which detects an undesirable presence may be used such as for example a motion, infrared, microwave, or sound detector. Though security and surveillance system 10 is shown having four zones of surveillance, security and surveillance system 10 may monitor any number of zones of surveillance and each zone of surveillance may have any number of detectors associated therewith. Security and surveillance system 10 also includes video cameras 20, 22, 24, and 26 for each zone of surveillance A, B, C, and D, respectively. Each zone of surveillance A, B, C, and D may also have light assemblies 28, 30, 32, and 34, respectively, associated therewith.

20 A television monitor 36 displays the images generated by cameras 20, 22, 24, and 26 by receiving a camera signal as determined by a camera switcher 38. Camera switcher 38 receives activation signals from detectors 12, 14, 16, and 18 and transmits an appropriate camera signal to television monitor 36 in response to an activation signal indicating that one of the detectors has determined the existence of a presence in a corresponding zone of surveillance. A 25 processor 40 also receives activation signals from detectors 12, 14, 16, and 18 and controls television monitor 36 and light assemblies 28, 30, 32, and 34.

30 In operation, detectors 12, 14, 16, and 18 determine the existence of an undesirable presence in zones of surveillance A, B, C, and D, respectively. A detector determining the existence of a presence, such as an intruder, generates an activation signal received by camera switcher 38. Camera switcher 38 activates one of video cameras 20, 22, 24, and 26 corresponding to the activation

6

5 signal and transmits the appropriate camera signal to television monitor 36. Processor 40 receives the triggered activation signal from the triggered detector through camera switcher 38. Processor 40 controls light assemblies 10 28, 30, 32, and 34 corresponding to the triggered zone of surveillance and generates a timed infrared code to control television monitor 36. Processor 40 may be programmed to generate a timed infrared code that can perform any of a variety of specific functions. An example shown in FIGURE 15 1 has processor 40 generating a timed infrared code to control television monitor 36 in order to display the image from the camera in a picture in picture window corresponding to the triggered zone of surveillance.

20 FIGURE 2 is a block diagram of processor 40. Processor 40 includes an infrared control board 50 comprising an infrared emitter 52 and an infrared sensor 54. Processor 40 also includes a pulse timer 56 and an event timer 58 connected in series to infrared emitter 52. Though not shown, processor 40 may also contain power supplies and transformers to drive detectors 12, 14, 16, and 18.

25 In operation, an activation signal from a detector in a triggered zone of surveillance initiates timer 58 which sends a signal to initiate timer 56, providing an input to infrared emitter 52. Infrared emitter 52, as programmed through a code learning access port 60, emits an infrared coded signal in response to the activation signal feeding through timer 58 and timer 56. Event timer 56 generates a signal for a predetermined time interval in response to the activation signal in order to drive infrared emitter 52. Event timer 58 prevents repeated activation signals from the same detector from affecting the ability of infrared emitter 52 to generate the appropriate infrared coded signal. Infrared sensor 54 transmits the infrared coded signal from infrared emitter 52 and provides the infrared coded signal to a secondary emitter 62 of FIGURE 1 for

5 controlling television monitor 36. Though infrared emitter 52 can control television monitor 36 alone, infrared sensor 54 and secondary emitter 62 provide for infrared coded signal transmission when processor 40 and television monitor 36 cannot be placed within operable vicinity of each other.

10 FIGURE 3 is a block diagram of another application of security and surveillance system 10. In this application, processor 40 can operate in one of three different modes. In the off mode, processor 40 does not generate an infrared code and thus security and surveillance system 10 does not display an image from a video camera onto television monitor 36.

15 In the viewing interrupt mode, processor 40 sends commands to television monitor 36 upon the detection of an intrusion event that will interrupt the normal viewing of entertainment programs on television monitor 36 by displaying the image generated by the camera in the triggered zone of surveillance. An example of a viewing 20 interrupt is shown in FIGURE 1 where processor 40 commands television monitor 36 to display the image from the triggered zone of surveillance on a picture in picture window.

25 The application of FIGURE 3 shows how television monitor 36 can display the image from a triggered zone of surveillance without having picture in picture capability. The camera signal from camera switcher 38 is fed through a UHF modulator 64 before being sent to television monitor 36. UHF modulator 64 converts the camera signal from camera switcher 38 into a UHF signal that can be displayed 30 on television monitor 36 over a selected UHF channel. During viewing interrupt mode, processor 40 commands television monitor 36 to proceed to the appropriate UHF channel for display of the image of the triggered zone of surveillance.

35

*f*

5 Boards 1, 2, and 3 within processor 40 generate separate infrared coded signals to control television monitor 36. During viewing interrupt mode, board 1 sends the infrared coded signal for the first digit of the UHF channel, board 2 sends the infrared coded signal for the second digit of the UHF channel, and board 3 is inactive. In this manner, processor 40 commands television monitor 36 to proceed to the predetermined UHF channel for displaying of the triggered zone of surveillance. Though each board 10 is shown as generating a separate infrared coded signal, processor 40 may include boards that generate multiple infrared coded signals as desired. Further, these boards may generate infrared coded signals which perform specific functions different than those shown in the examples of 15 FIGURE 3.

During non-viewing periods, processor 40 may be placed in an alert mode. In the event of an intrusion event during alert mode, processor 40 commands television monitor 36 to power on and subsequently display the image from the triggered zone of surveillance on an appropriate UHF channel or picture in picture window. For the specific application shown, board 1 generates an "on" infrared coded signal, board 2 generates an infrared coded signal for the first UHF channel digit, and board 3 generates an infrared coded signal for the second UHF channel digit. As another example of a specific function, processor 40 may also adjust the sound level of television monitor 36 to provide an audio alert in the event of an intruder detection.

30 Camera switcher 38 may be programmed to transmit selected images to television monitor 36. In the event two or more detectors generate activation signals, camera switcher 38 can transmit images serially through timed intervals. Camera switcher 38 may also transmit a plurality of images simultaneously in a multiple display format such as split screen or quad screen. In this 35

manner, security and surveillance device 10 can provide efficient monitoring of the zones of surveillance.

In summary, a security and surveillance system uses a processor to generate an infrared code in order to perform a specific function. The processor receives an activation signal from a detector monitoring his own surveillance. Processor 40 may be programmed to generate infrared codes corresponding to the activation signal for performing a variety of different functions. This specific application 5 has the processor generating infrared codes to control a television monitor which can display images generated by video cameras located at each zone of surveillance. Processor 40 may be programmed to interrupt normal viewing operation of the television monitor or energize the television monitor to provide an alert warning during non 10 viewing periods.

Thus, it is apparent that there has been provided, in accordance with the present invention, a security and surveillance system that satisfies the advantages set forth 15 above. Although the preferred embodiment has been described in detail, it should be understood that various changes, substitutions, and alterations can be made herein. For example, though the processor is shown to control a television monitor in the preferred embodiment, the 20 processor may generate infrared codes which can control devices other than a television monitor such as a localized alarm. Other examples are readily ascertainable by one skilled in the art and could be made without departing from the spirit and scope of the present invention as defined by 25 the following claims.

10